

Electronic Acknowledgement Receipt

EFS ID:	1793125
Application Number:	10756419
International Application Number:	
Confirmation Number:	4551
Title of Invention:	MANUFACTURE OF A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A PLUARALITY OF A COLUMNAR LAMINATES HAVING DIFFERENT SPACING IN DIFFERENT DIRECTIONS
First Named Inventor/Applicant Name:	Tatsunori Murata
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Filer:	Gregory Edwards Montone/Ricardo Perez
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Application Type:	Utility

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)

1	Issue Fee Payment (PTO-85B)	43228X00PartB.pdf	31907	no	1
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8562	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				40469	
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